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PATENT ABSTRACTS OF JAPAN

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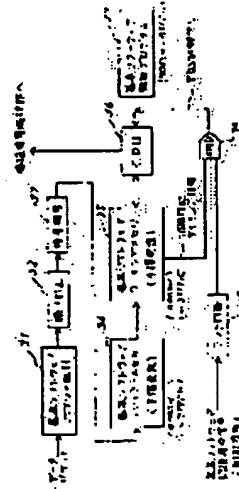
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(54) DECODER CONTROL SYSTEM

(57)Abstract:

PURPOSE: To change the performance of a receiver by changing basic software, by providing a memory in which new basic software is stored and the memory in which the software current in use is stored.

CONSTITUTION: Received new software is stored in a buffer memory 34. A latch circuit 38 latches a basic software switching command signal from a broadcasting station side at the time of receiving a basic software switching command. Interruption is applied on a CPU 36 when a switchable timing signal is set a high level, then, a basic software transfer program 37 is executed. The transfer program resets an AND39, and inspects the content of the new basic software by using an erroneous qualifier, etc. When a satisfactory inspection result can be obtained, the new basic software in the memory 34 is transferred to a working memory 35, and control is delivered to an OOOOH, then, the new basic software is started. When no satisfactory inspection result is obtained, no transferr is performed.



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